

Errata

Dual-Thread Speculation: Two Threads in the Machine are Worth Eight in The Bush
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Figure 5b: The speedups for the CMP (ie *4t 2i* models) are wrong. Accidentally, speedups without run-length prediction were used in this graph. *The speedups for the CMP model in figure 5b should be the same as those in 5a.*

Figure 7: The word both is missing in the legends. *The speedups for the third bar in each group is for both module- and loop-level parallelism in figures 7a and 7b.*